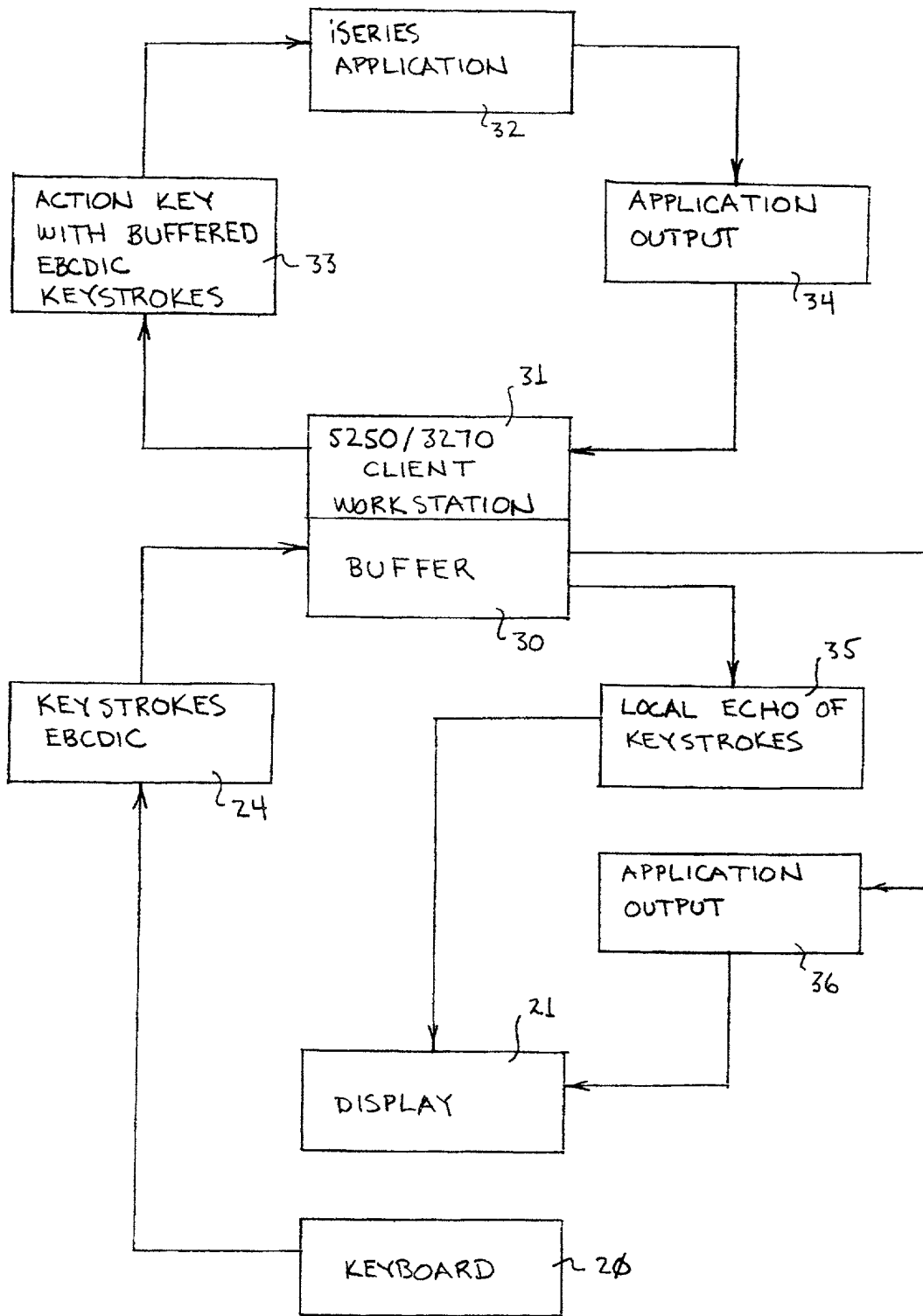


FULL DUPLEX CHARACTER MODE (VTxxx)

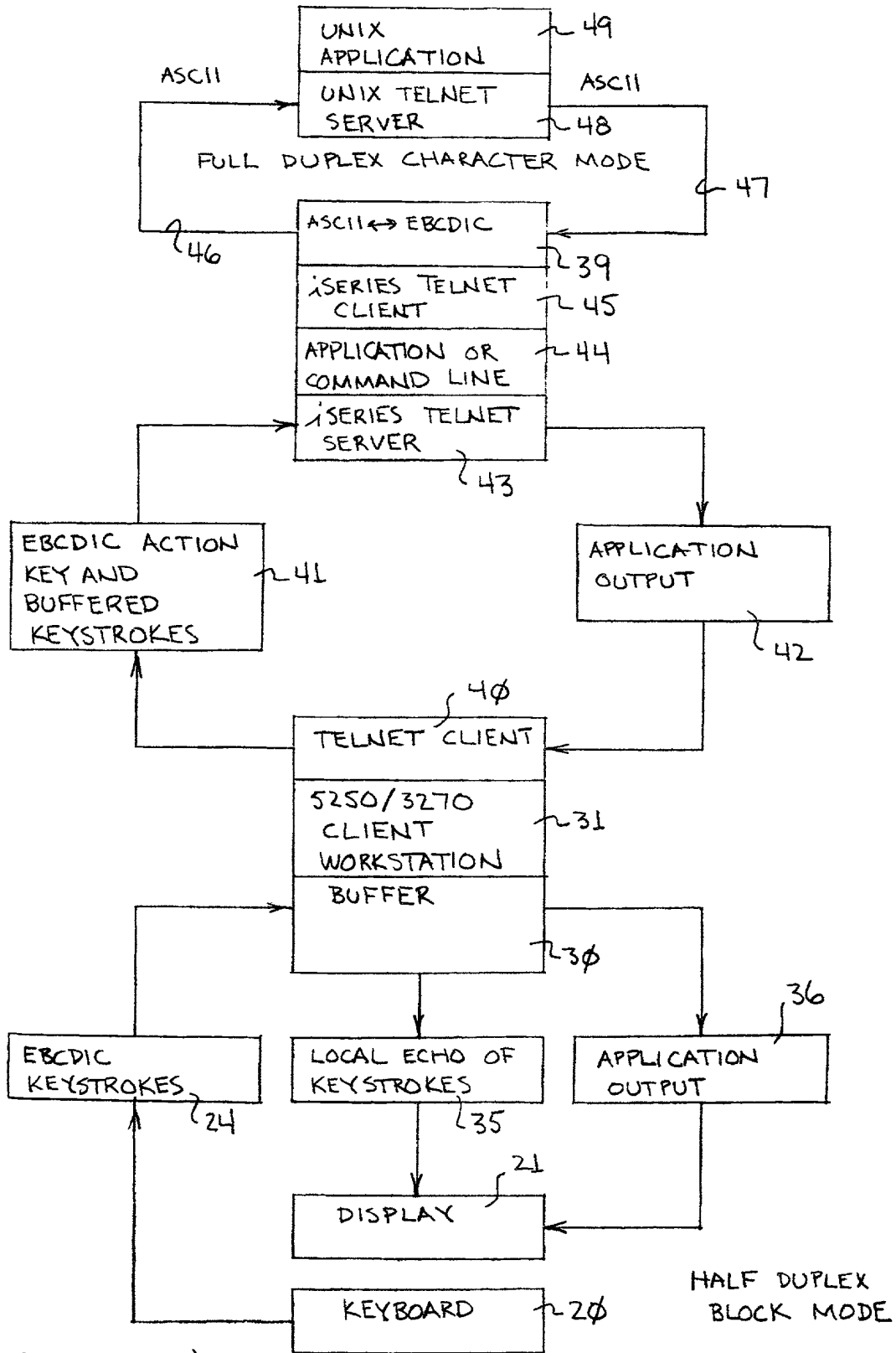
(PRIOR ART)

FIG. 1



HALF DUPLEX BLOCK MODE
(PRIOR ART)

FIG. 2



(PRIOR ART)

FIG. 3

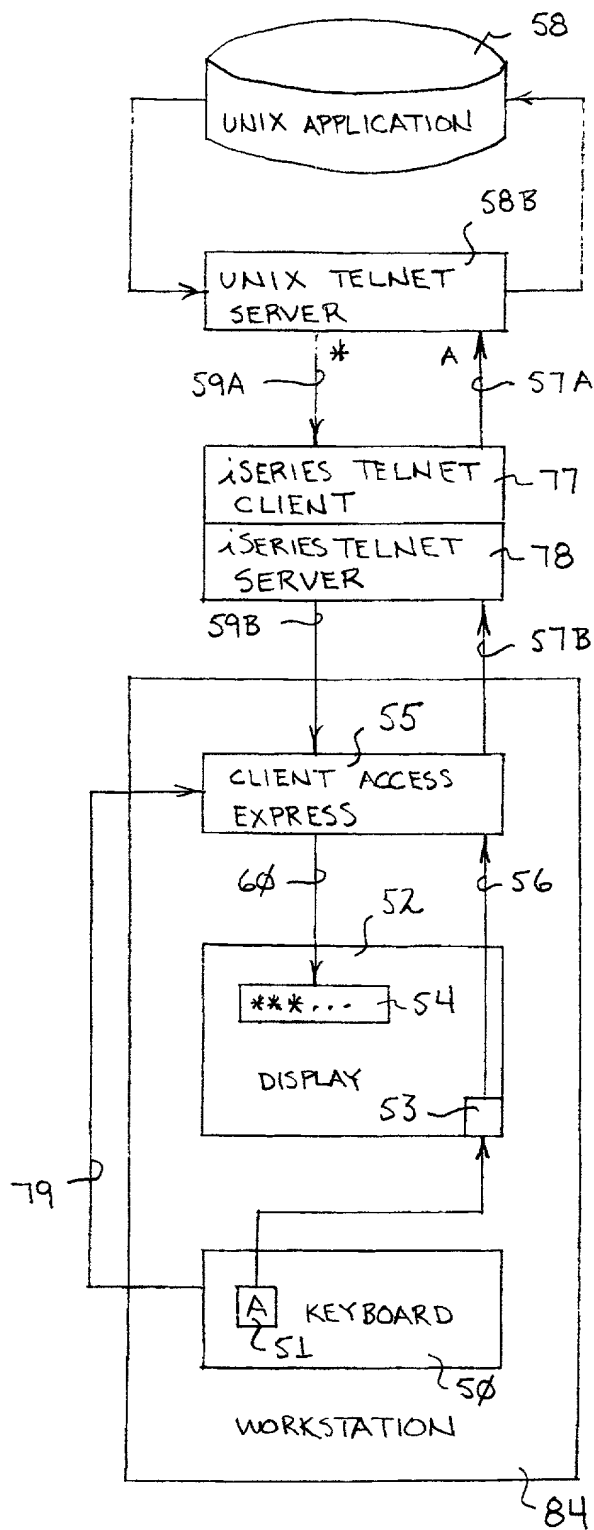


FIG. 4

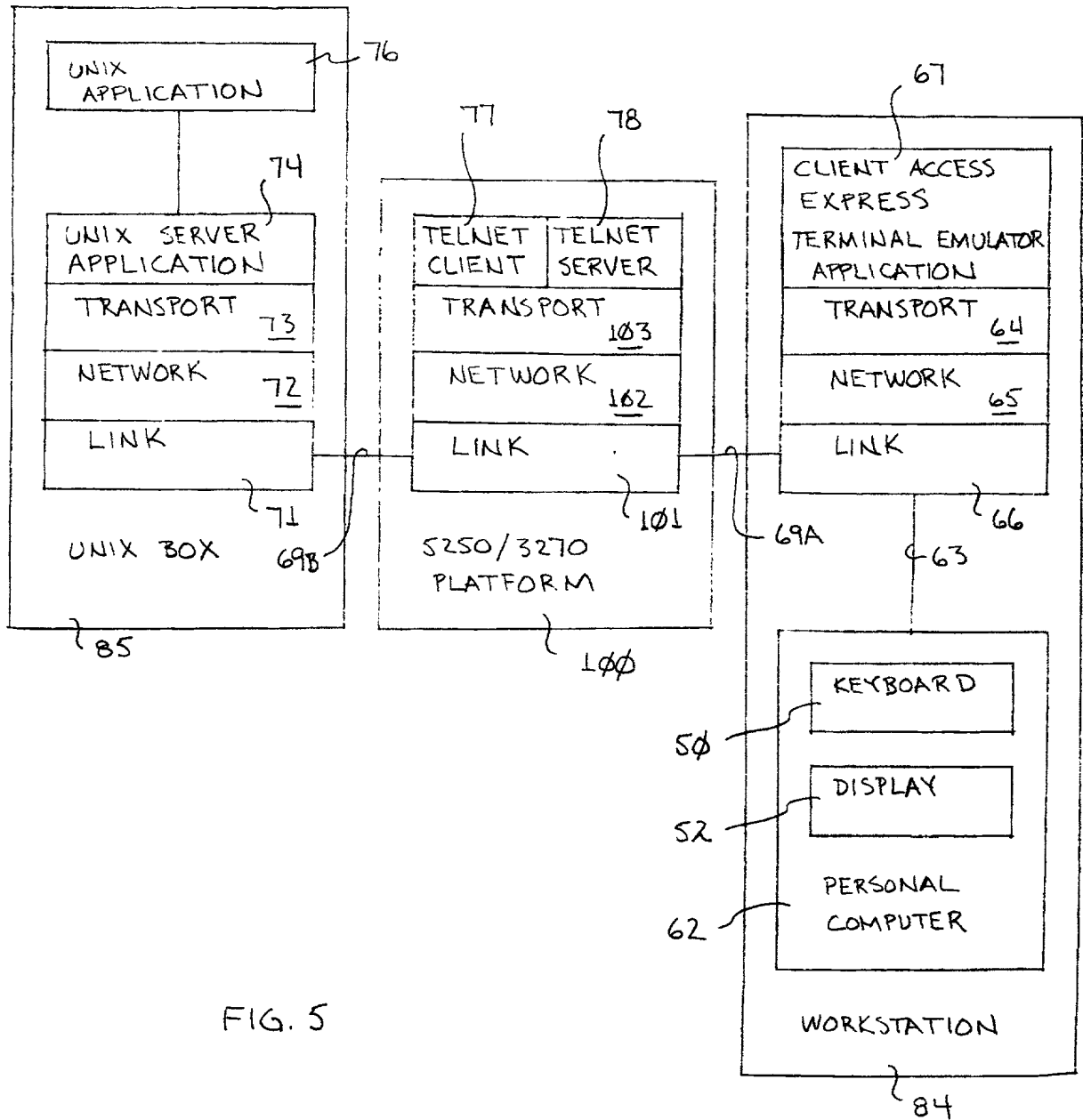


FIG. 5

1

EXAMPLE IMPLEMENTATION

FIG. 6

FIG. 6